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**INFORMATION DISCLOSURE
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| Application Number | 09/945121 |
| Filing Date | August 30, 2001 |
| First Named Inventor | Shirley, Paul |
| Group Art Unit | 1744 |
| Examiner Name | Till, Terrence |

Sheet 1 of 1

Attorney Docket No: 303.774US1

US PATENT DOCUMENTS

| Examiner Initial * | USP Document Number | Publication Date | Name of Patentee or Applicant of cited Document | Class | Subclass | Filing Date If Appropriate |
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OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

| Examiner Initials * | Cite No ¹ | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-Issue number(s), publisher, city and/or country where published. | T ² |
|---------------------|----------------------|---|----------------|
| TJ | | VAN ZANT, PETER, "Microchip fabrication : a practical guide to semiconductor processing", 4th Edition, New York : McGraw-Hill, Chapter 4 & Chapters 8-9, (2000), 87-132 & 193-280 | |

EXAMINER

Terrence Till

DATE CONSIDERED

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Substitute Disclosure Statement Form (PTO-1449)

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